

FIGURE 2

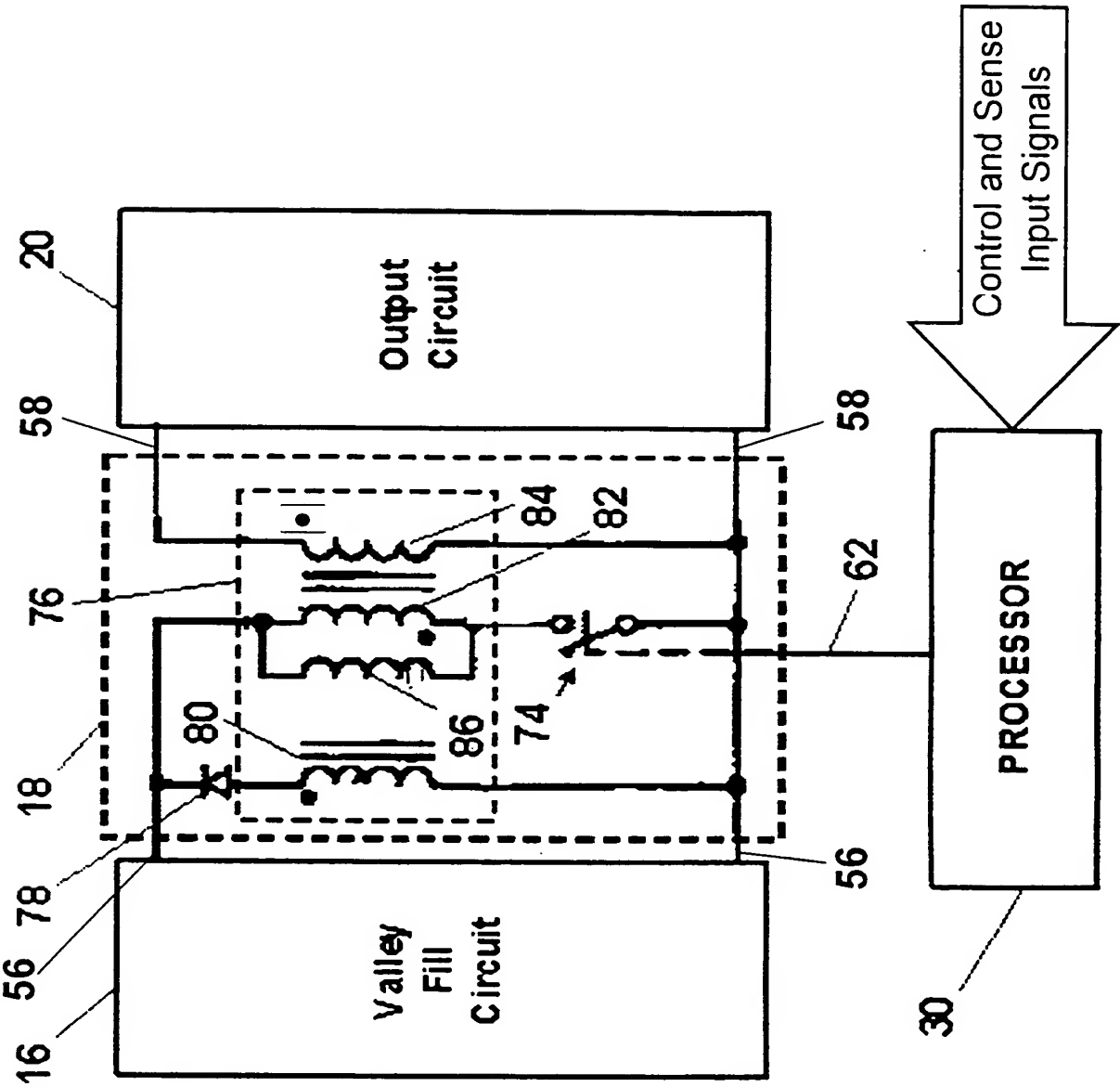
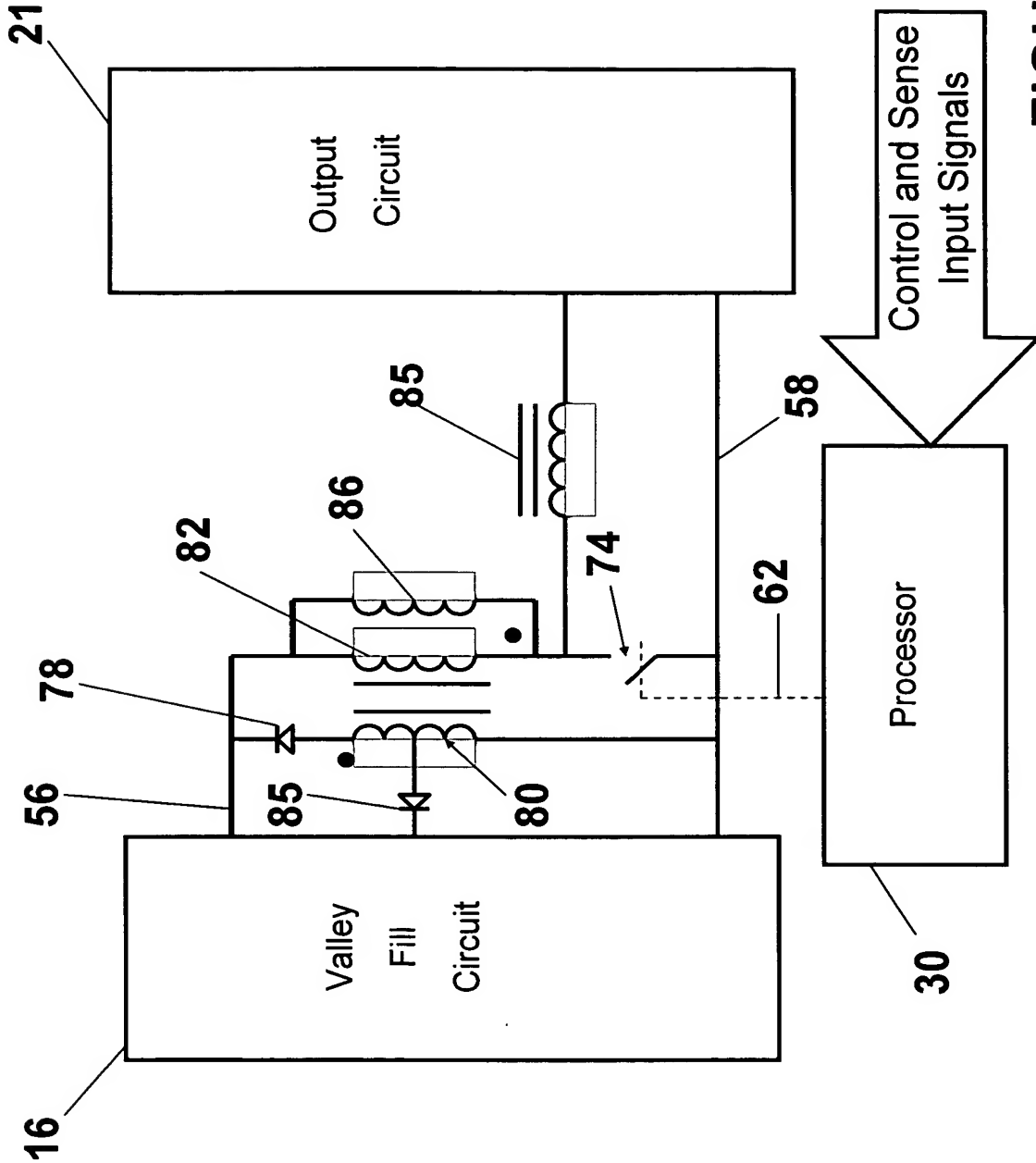


FIGURE 3A

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## PROCESSOR CONTROLLED BALLAST STATES

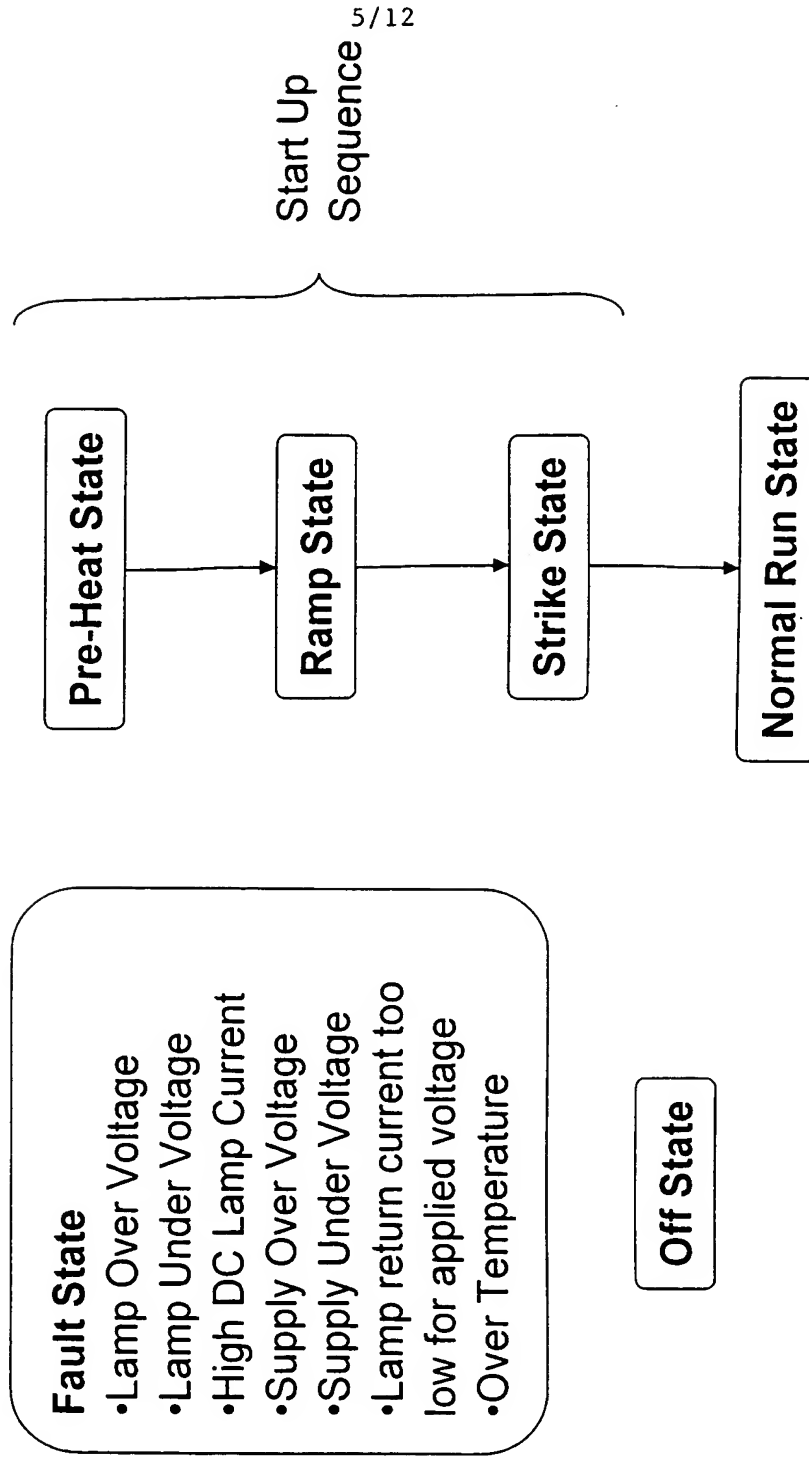


FIGURE 4

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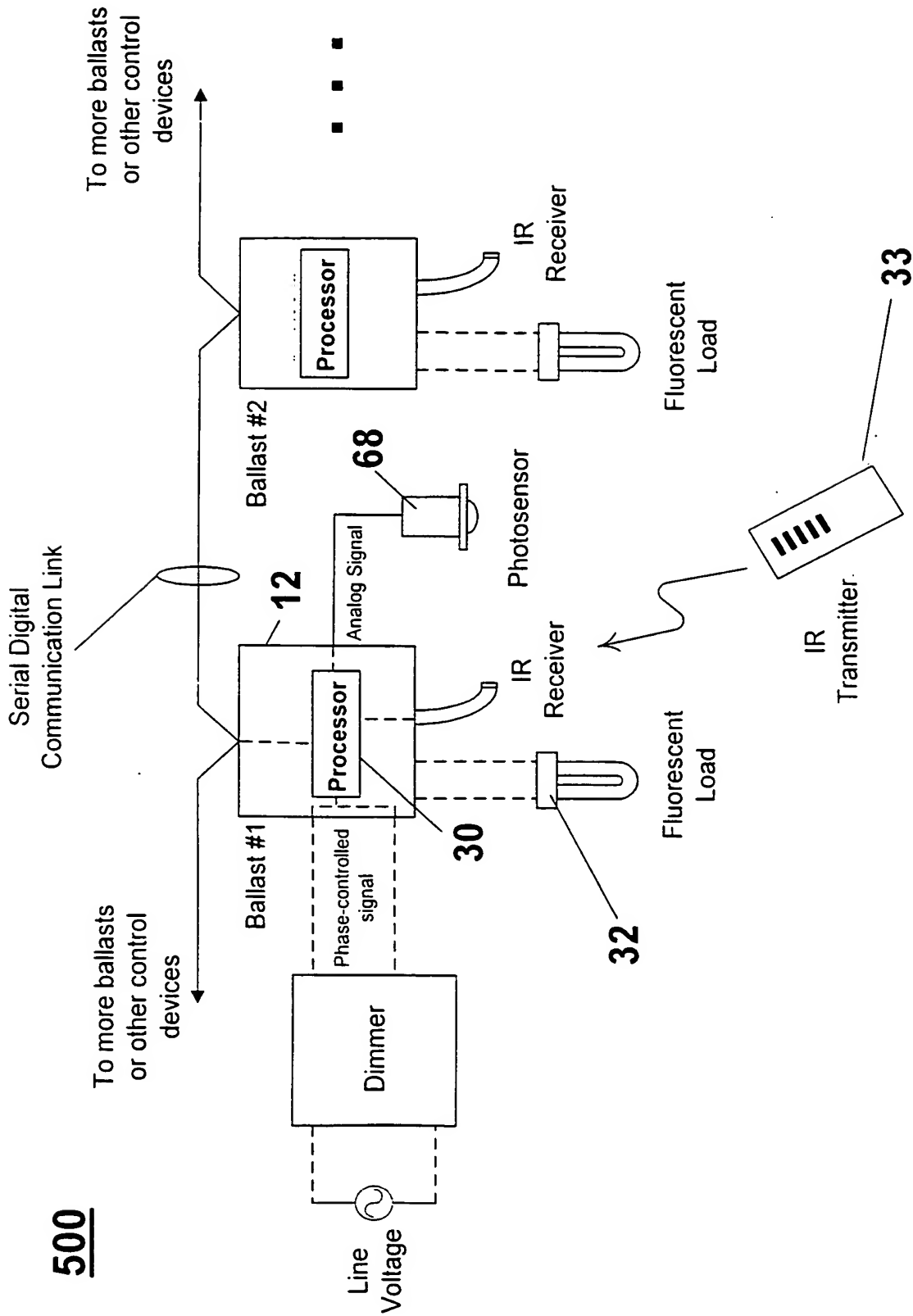
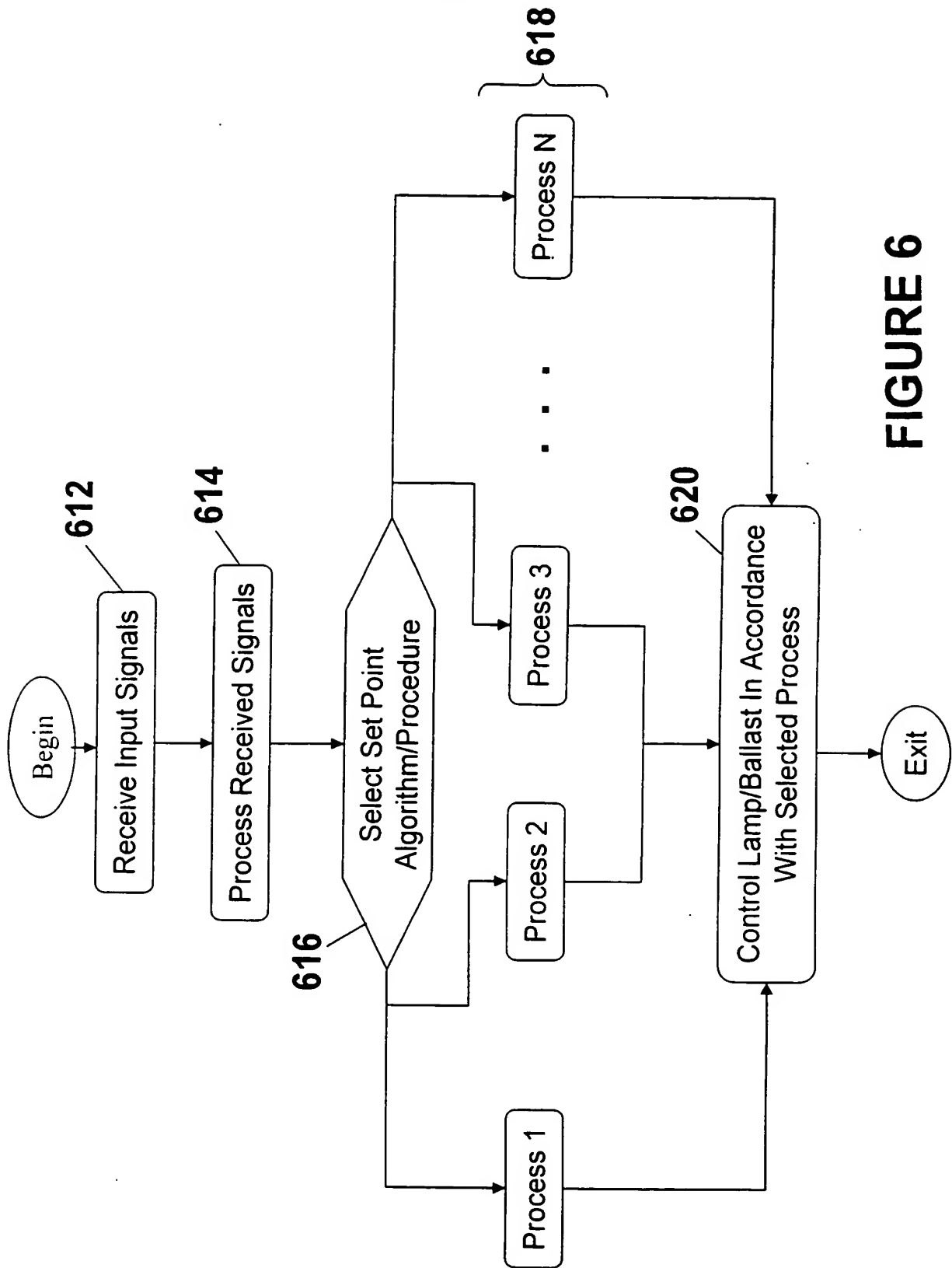
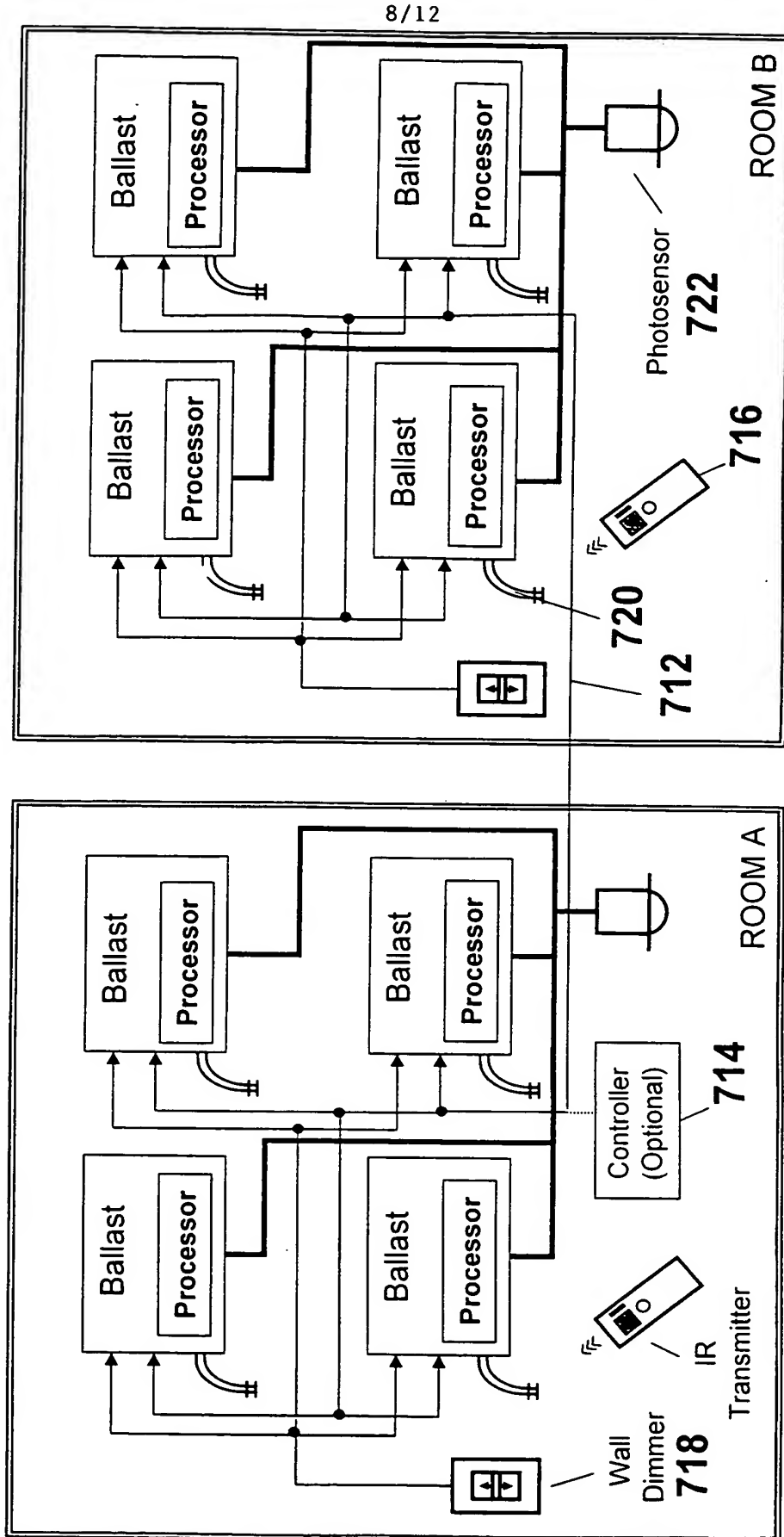


FIGURE 5

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**700**



**FIGURE 7**



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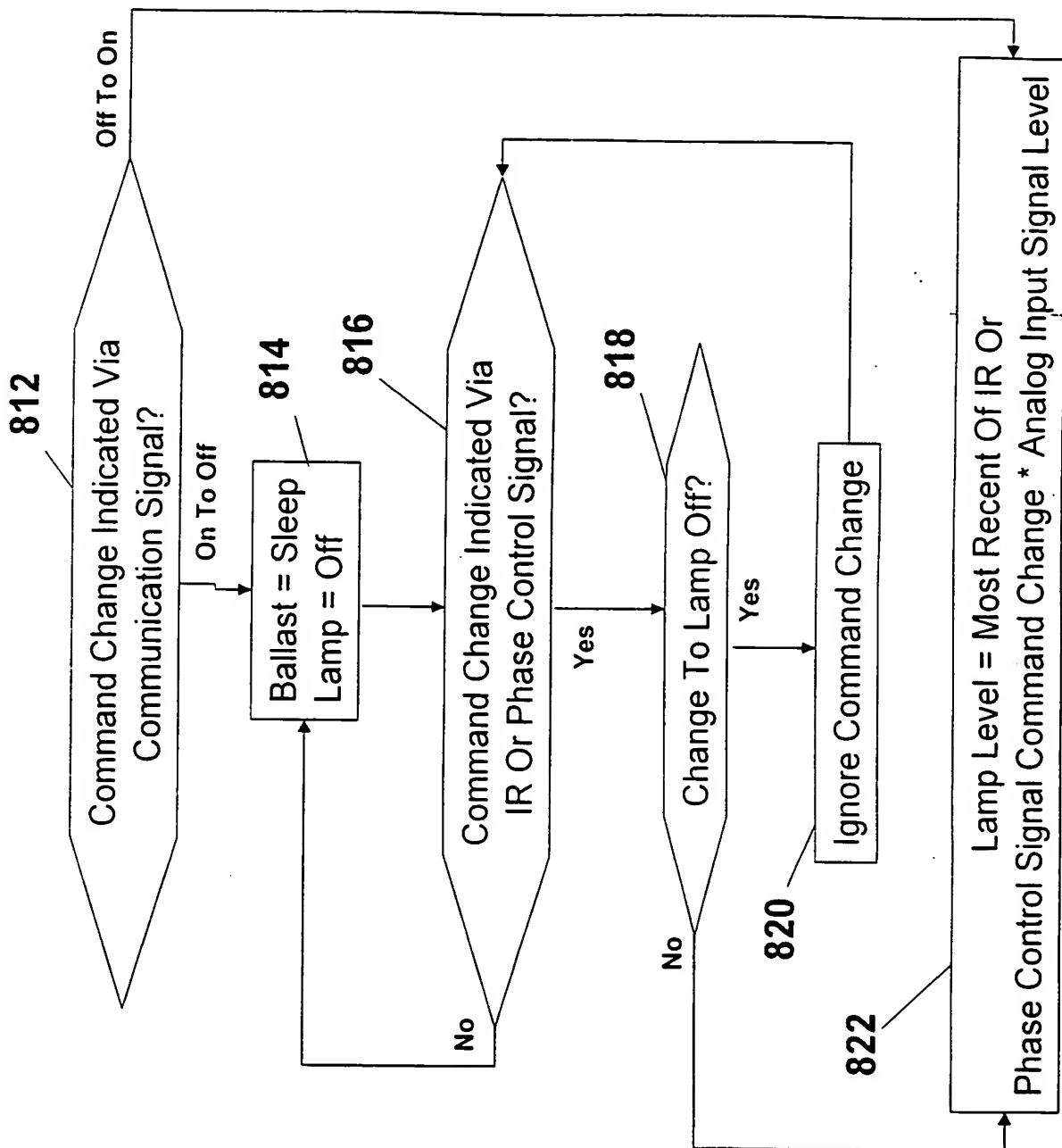
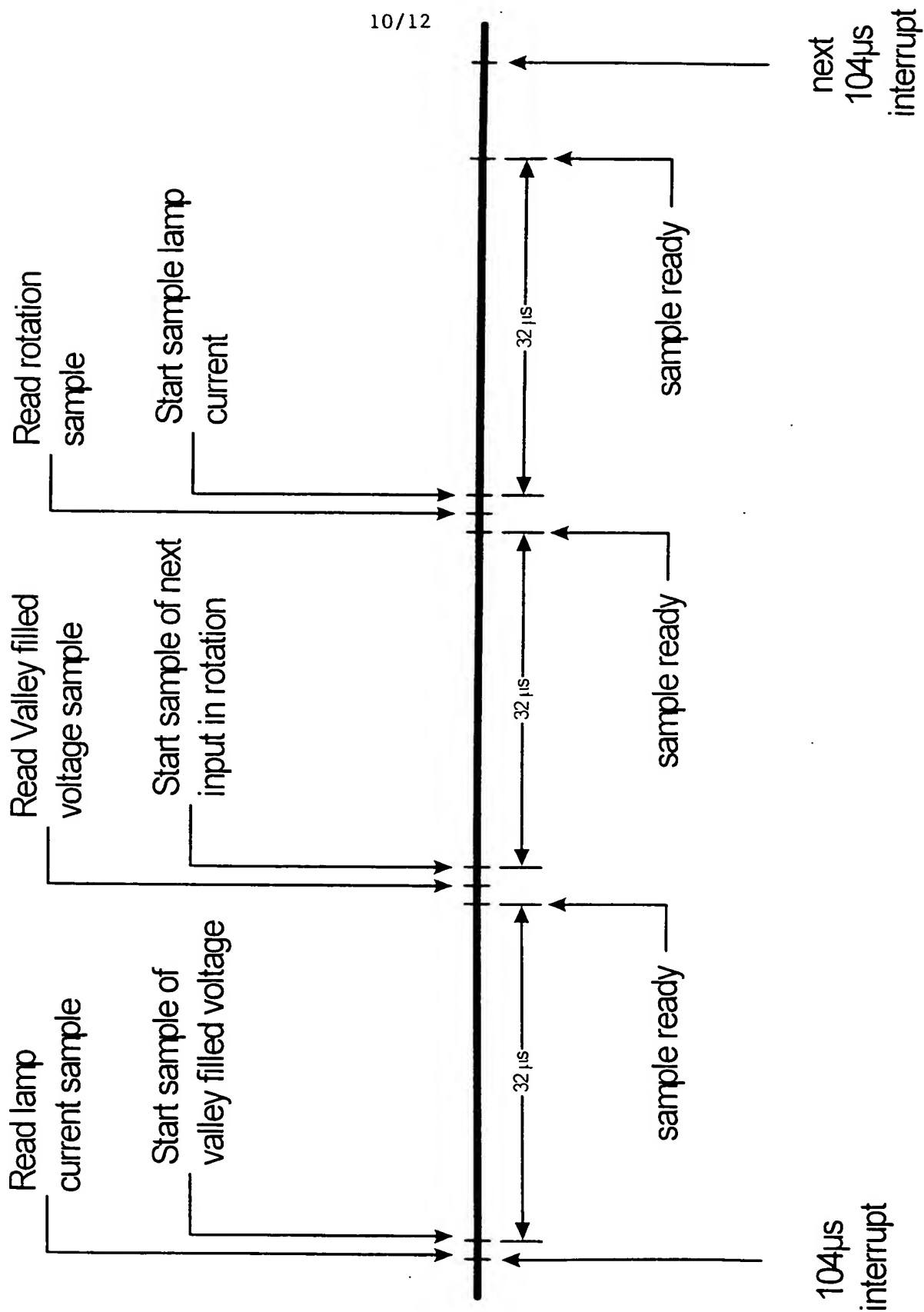


FIGURE 8



**FIGURE 9**

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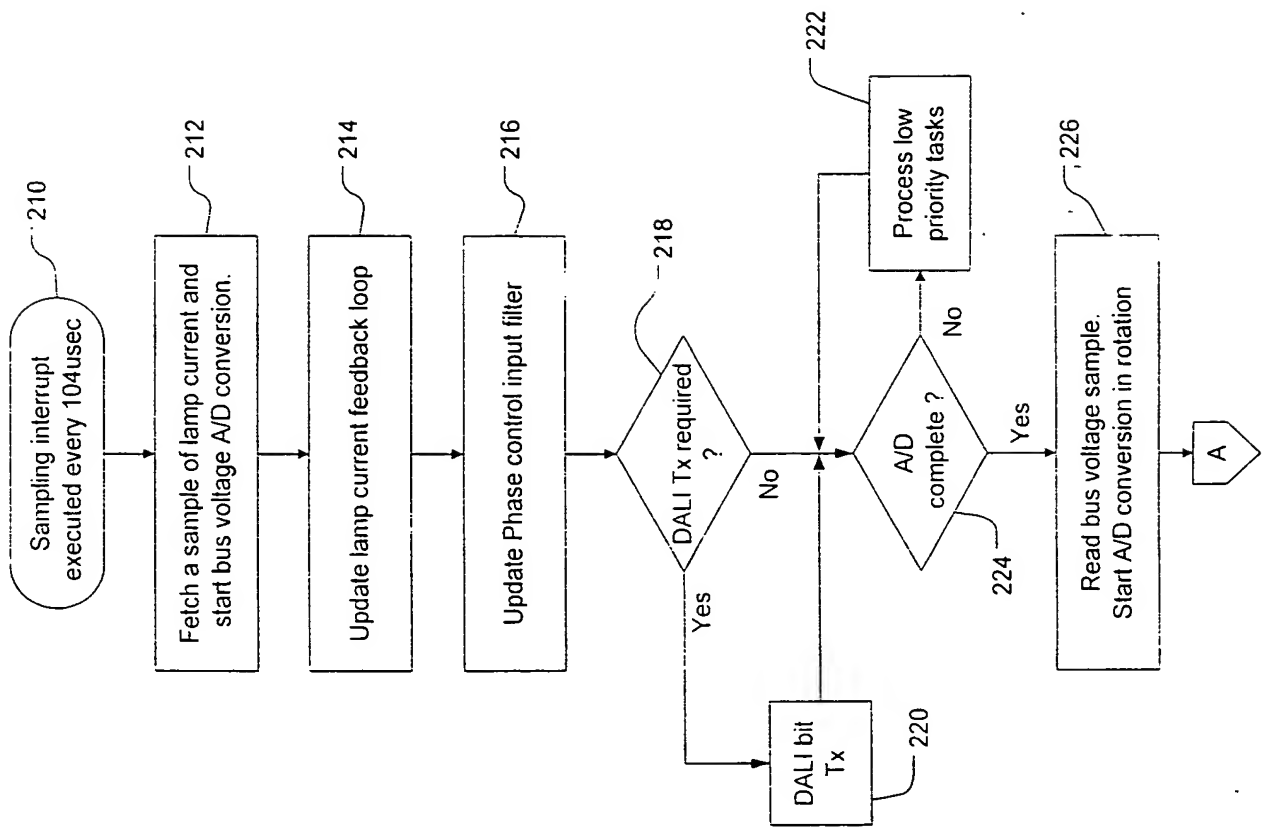


FIGURE 10A

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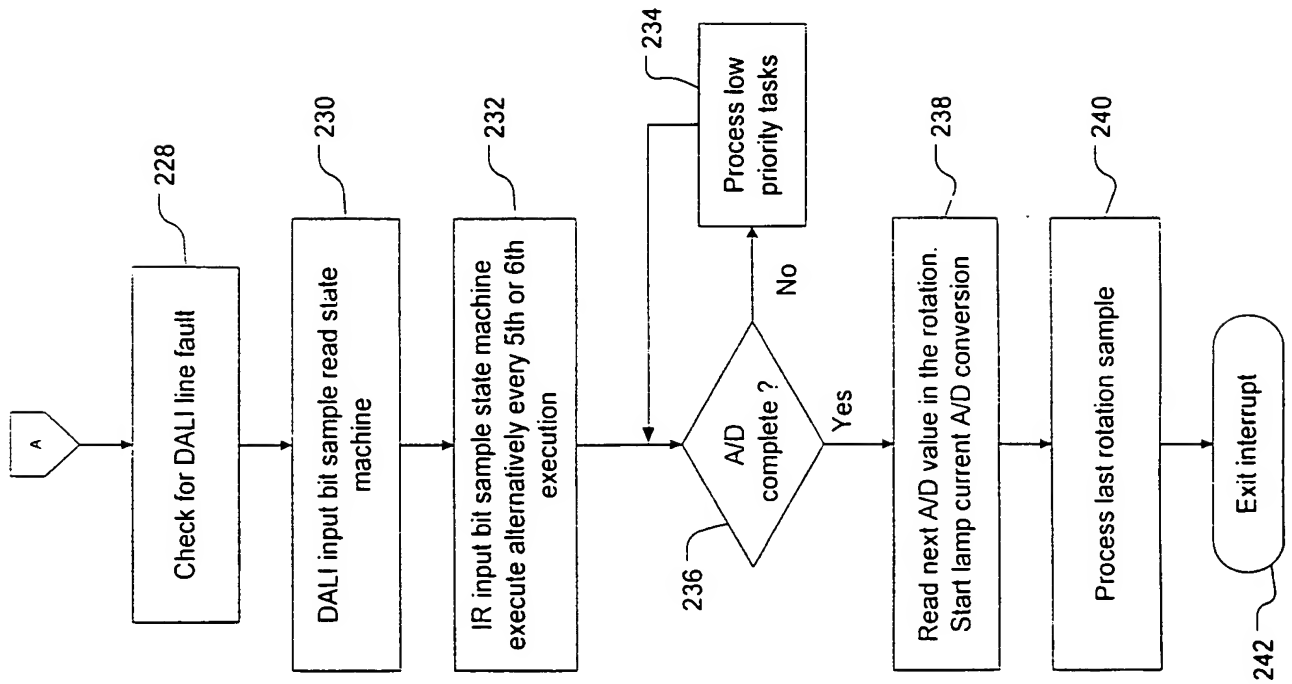


FIGURE 10B